

WHAT IS CLAIMED IS:

1. A method for producing an output signal that is delayed compared to an input signal, comprising:

forming at least first and second intermediate signals delayed differently with
5 respect to an input signal;

combining the first and second intermediate signals to form an output signal having a rising edge that corresponds to an edge of the first intermediate signal and having a falling edge that corresponds to an edge of the second intermediate signal.

2. The method according to claim 1, wherein said forming step includes
10 performing successive delay of the input signal to form the intermediate signals.

3. The method according to claim 2, wherein said forming step includes forming $2k$ intermediate signals respectively delayed around $360^\circ/(2k)$ compared to the input signal, wherein k is a natural number, and combining pairs of the intermediate signals whose delay relative to one another is around 180° to form
15 respectively corresponding output signals.

4. The method according to claim 3, wherein said combining step includes, in response to a control signal, using one of the first intermediate signal and an inversion of the second intermediate signal to form the output signal.

5. The method according to claim 4, wherein k is an even number and the
20 control signal is one of the intermediate signals that has an absolute delay of around

90° with respect to both the first intermediate signal and the second intermediate signal.

6. The method according to claim 1, wherein said forming step includes regulating delay so that the intermediate signal with the largest delay experiences a
5 delay of around 360° compared to the input signal.

7. The method according to claim 6, wherein said regulating step includes regulating delay depending on the input signal, the intermediate signal with the largest delay, and a further one of the intermediate signals.

8. The method according to claim 7, wherein the further intermediate
10 signal has a delay of around 90° compared to the input signal.

9. The method according to claim 7, wherein said regulation step includes a first delay regulation signal reducing the delay of all intermediate signals, and a second delay regulation signal increasing the delay of all intermediate signals.

10. Method according to claim 9, wherein said regulating step includes generating the delay regulation signals according to the following relationship

Input Signal	Further Intermediate Signal	Intermediate Signal with Greatest Delay	First Delay Regulation Signal	Second Delay Regulation Signal
0	0	0	0	0
1	0	0	1	0
0	1	0	0	0
0	0	1	0	1
1	1	0	0	0
1	0	1	1	1
0	1	1	0	0
1	1	1	0	0

wherein 1 corresponds to a first signal level and 0 corresponds to a second signal level.

5 11. The method according to claim 1, including delaying an input timing signal to obtain the input signal.

12. The method according to Claim 1, wherein said combining step includes, in response to a control signal, using one of the first intermediate signal and an inversion of the second intermediate signal to form the output signal.

10 13. The method according to Claim 12, wherein the control signal is one of the intermediate signals that has an absolute delay of around 90° with respect to both the first intermediate signal and the second intermediate signal.

14. A device for producing an output signal that is delayed compared to an input signal, comprising:

an input for receiving the input signal;

at least two delay elements coupled to the input for producing respective
5 intermediate signals in response to the input signal;

a circuit connected to the delay elements to form from first and second ones of the intermediate signals an output signal having a rising edge that corresponds to an edge of the first intermediate signal and having a falling edge that corresponds to an edge of the second intermediate signal.

10 15. The device according to claim 14, wherein the delay elements are connected in series.

16. The device according to claim 15, including $2k$ of the delay elements connected in series for producing respective intermediate signals, each of the delay elements for producing a delay of around $360^\circ/(2k)$, wherein k is a natural number,
15 and including k of said circuits connected to the delay elements, each of the circuits for receiving a pair of the intermediate signals that are delayed with respect to one another by around 180° and for forming therefrom a corresponding output signal.

17. The device according to claim 14, wherein each of the circuits comprises a multiplexer.

18. The device according to claim 14, wherein the delay elements are controllable, and including a delay regulation device coupled to the delay elements for controlling the delay elements so that the intermediate signal with the largest delay has a delay of around 360° compared to the input signal.